

Ing. Radek Holý, Ph.D., Ing.Paed.IGIP, Feng. – Curriculum Vitae

Narozen: 1976

ORCID iD 0000-0002-8604-2791

Contact: Faculty of Transportation Sciences (FTS), Czech Technical University in Prague (CTU)
Konviktská 20, Prague, Czech Republic

Qualification:

- 2001 Ing. FEE CTU, Czech Republic
- 2019 Ph.D. FTS CTU, Czech Republic
- 2020 International Chair Professor, NTUT, Taiwan
- 2024 Ing.Paed.IGIP, International Society for Engineering Pedagogy
- 2025 Chair of Innovation, AISSM, NCKU, Taiwan
- 2025 Chair of Industry-Academia Innovation, INNC, NTUST, Taiwan

Work experience:

- 2018 – Vice-Rector for Quality Management, CTU in Prague, Czech Republic
- 2012 - Assistant Professor, FTS, CTU in Prague, Czech Republic
- 2006 – 2018 - Head of Department of IS, CIC, CTU in Prague, Czech Republic
- 2003 – 2005 - Deputy Head of Department of IT, FE, CUNI in Prague, Czech Republic
- 2002 – 2014 - Researcher, CESNET, Czech Republic
- 2002 – 2014 - Assistant Professor, FE, CUNI in Prague, Czech Republic
- 2003 – 2010 - Network administrator, CC, CUNI in Prague, Czech Republic

Research interests:

- Authentication Technology
- Information Security
- Mining Engineering
- Smart Cities

Academic Services:

- International Advisory Committee, The International Symposium on Information Theory and Its Applications (ISITA), Taipei, Taiwan, 2024
- Scientific Committee, International Conference on Polymer Science and Technology (SPSI-MACRO), India, 2023
- International Program Committee, Conference on Technology, Informatics, Management, Engineering & Environment (TIME-E), Bali, Indonesia, 2024
- Scientific Committee, Computer science, Jihlava, Czech Republic, 2023
- Program Committee, MoodleMoot.cz, České Budějovice, Czech Republic, 2023
- Scientific Committee, Horizons of railway transport, Demänovská Dolina, Slovakia, 2025
- Keynote Speaker, CZECH CYBER SECURITY EXCELLENCE, Haag, Netherlands, 2023
- Keynote Speaker, XX. Annual Conference on Information Security (Tate international), Prague, Czech Republic, 2019
- Keynote Speaker, EUNIS, Špindlerův mlýn, Czech Republic, 2016

Other activities:

- EUNIS-CZ - Board member
- ECCA - Vice President Board of Directors
- Cesnet - Vice chairman Board of Directors

- The Engineering Academy of the Czech Republic - Member
- Czech National Semiconductor Cluster -Vice chairman
- ACDRC (Advanced Chip Design and Research Center) – Director
- TSRI -CTU Joint Research Center – director
- Czech Semiconductor center - Member Steering committee